

LEXOR

A division of customDesignTechnologies Ltd

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Unit B, Nigel Court, Ward Road, Buckingham Road Industrial Estate, Brackley, NN13 7LF, United Kingdom

Dual-In-Line Tapped Delay Network "Dipline" 24 Pin Series J - Surface Mount

Basic Specification

Delay Range	100 nS to 4 μ S \pm 5%
Impedance Range	50 Ω to 560 Ω \pm 10%
Number of Sections	40 'M' - derived 'T' sections
Taps	20 at 5% intervals
Rise-time	Better than 9% of Total Delay
Distortion	\pm 10% Max.
Attenuation	1.0dB Max
Working Voltage	50V
Temperature Coefficient of Delay	\pm 200 p.p.m./ $^{\circ}$ C Max
Operating Temperature Range	-50 $^{\circ}$ C to +125 $^{\circ}$ C
Encapsulation	Flame Retardant Epoxy Resin

Selection Table

Delay Time, nS	50 Ω	75 Ω	100 Ω	200 Ω	500 Ω	560 Ω
100	-	-	007	011	-	-
150	075	091	102	116	132	146
200	033	003	008	012	060	147
250	076	004	103	027	067	017
300	077	092	104	117	133	148
350	078	049	105	118	134	149
400	024	059	009	119	061	150
450	079	093	106	120	135	151
500	056	031	057	037	043	018
550	080	094	107	121	136	152
600	081	095	108	122	137	072
650	082	006	109	123	138	153
700	083	096	110	124	139	025
800	084	097	111	125	062	154
900	085	098	112	126	140	155
1000	001	002	055	032	013	054
2000	086	099	040	127	141	016
3000	-	-	113	128	-	-
4000	-	-	114	129	-	-

Example; A Dipline 24 Pin Series J - Surface Mount, 500nS delay, 200 Ω Impedence, is ordered as J037G.

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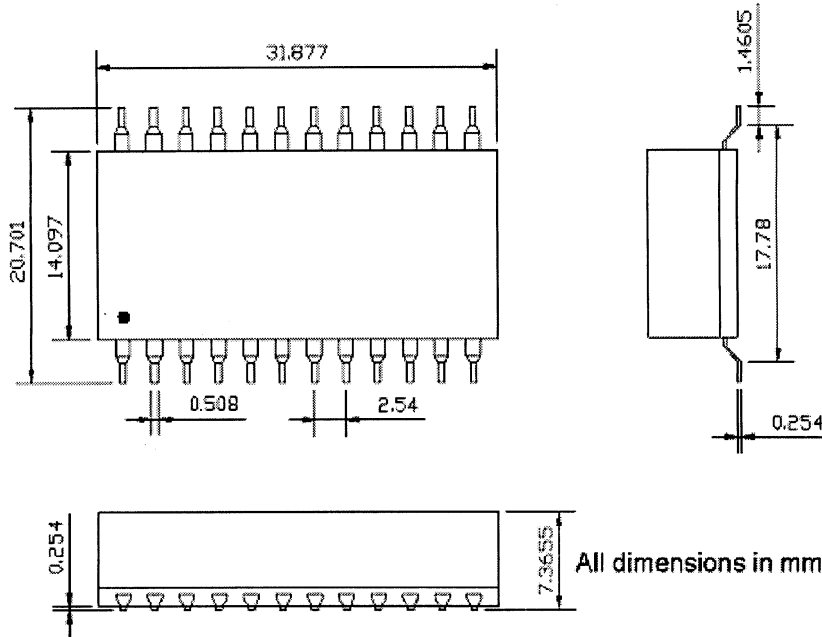
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Mechanical Detail



Pin No.	Tap No.	Delay %
1	Input	0
2	1	50
3	2	5
4	3	45
5	4	10
6	5	40
7	6	15
8	7	35
9	8	20
10	9	30
11	10	25
12	Earth	E
13	Not Connected	N.C
14	Not Connected	N.C
15	11	75
16	12	70
17	13	80
18	14	65
19	15	85
20	16	60
21	17	90
22	18	55
23	19	95
24	20	100