

# LEXOR

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Unit B, Nigel Court, Ward Road, Buckingham Road Industrial Estate, Brackley, NN13 7LF, United Kingdom

## Dual-In-Line Delay Networks for Video Applications

### Programmable

This series of passive, equalised Delay Lines has been specifically designed for professional video applications. They are mutually compatible and meet or exceed the specifications used for use in PAL Colour Television Systems.

Impedance -----	75Ω
Delay (Td) -----	see table
Tolerance on Delay -----	Td ≥ 50nS +0,-1.4%
	Td 20nS - 50nS +0,-4%
	Td ≤ 20nS +0,-2%
Return Loss -----	Better than -28dB w.r.t. 75Ω
Ripple -----	< 0.1dB, 15KHz to 5.5MHz
Frequency Response -----	±0.1dB to 5.5MHz
Group Delay -----	±5nS Max
K (2-T) -----	0.5% Max
Working Voltage -----	30V
Temperature Coefficient of Delay -----	±100 p.p.m/°C
Operating Temperature Range -----	-20°C to +80°C

Below table shows typical device values - alternative delay sections and pin-outs may also be available.

Device	Total Delay, nS	Case	Programmable sections	Equalised section(s)
VP1A 0009	9.5	14 pin 0.3"	0.5,1,2,2,4	-
VP1A 0075	75	14 pin 0.3"	5,10,20,40	40
VP1A 0155	155	14 pin 0.3"	5,10,20,40,80	80
VP3A 0155	155	24 pin 0.6"	5,10,20,40,80	40,80
VP3A 0315	315	24 pin 0.6"	5,10,20,40,80,160	80,160
VP4A 0095	95	36 pin 0.6"	5,10,20,20,40	40
VP4A 0155	155	36 pin 0.6"	5,10,20,40,80	40,80
VP4A 0235	235	36 pin 0.6"	5,10,20,40,80,80	40,80,80
VP4A 0315	315	36 pin 0.6"	5,10,20,40,80,160	40,80,160
VP4A 0335	335	36 pin 0.6"	5,10,20,40,80,80,100	80,80,100
VP6A 0009	9.5	16 pin 0.6"	0.5,1,2,2,4	-
VP6A 0050	50	16 pin 0.6"	1,2,2,5,10,10,20	20
VP6A 0075	75	16 pin 0.6"	5,10,20,40	40
VP6A 0095	95	16 pin 0.6"	5,10,20,20,40	40
VP6A 0155	155	16 pin 0.6"	5,10,20,40,80	80
VP7A 0075	75	28 pin 0.3"	5,10,20,40	40
VP7A 0095	95	28 pin 0.3"	5,10,20,20,40	40
VP7A 0115	155	28 pin 0.3"	5,10,20,40,80	80
VP7A 0235	235	28 pin 0.3"	5,10,20,40,80,80	80,80

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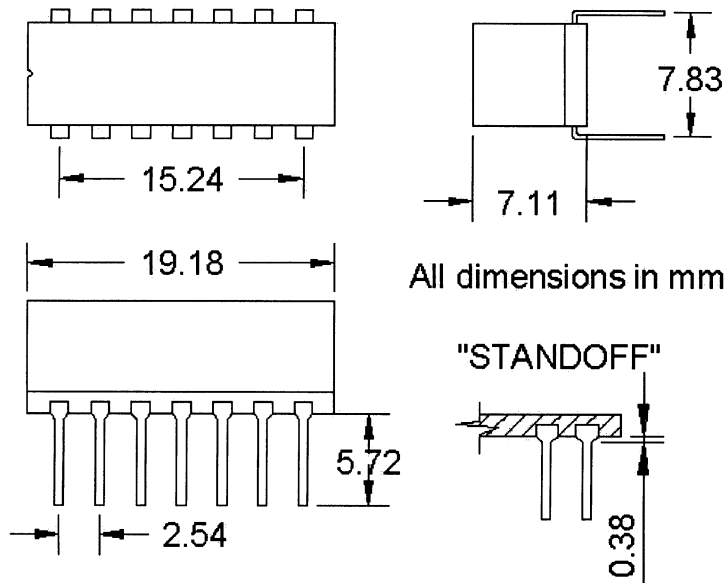
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## Dual-In-Line Delay Networks for Video Applications

### VP1 Dimensions & Pin Out



VP1A0009	SECTION	VP1A0075	SECTION	VP1A0155	SECTION
0.5	3 to 4	5	3 to 4	5	3 to 4
1	5 to 6	10	5 to 6	10	5 to 6
2	9 to 10	20	9 to 10	20	9 to 10
2	11 to 12	40	11 to 12	40	11 to 12
4	13 to 14	GND	1,2,7,8	80	13 to 14
GND	1,2,7,8			GND	1,2,7,8

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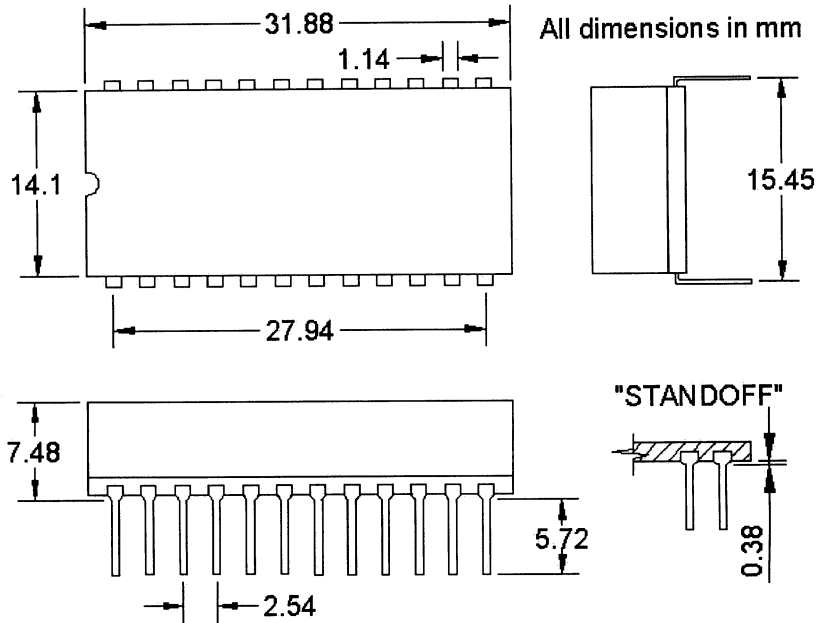
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## Dual-In-Line Delay Networks for Video Applications

### VP3 Dimensions & Pin Out



VP3A0155	SECTION	VP3A0315	SECTION
5	13 to 14	5	13 to 14
10	15 to 16	10	15 to 16
20	17 to 18	20	17 to 18
40	19 to 20	40	19 to 20
80	21 to 22	80	21 to 22
GND	1 to 12	160	23 to 24
		GND	1 to 12

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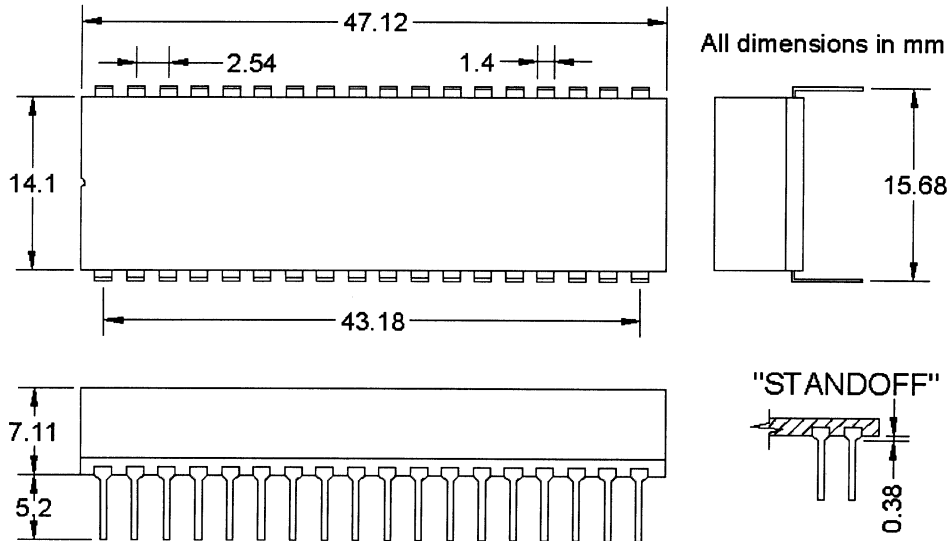
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## Dual-In-Line Delay Networks for Video Applications

### VP7 Dimensions & Pin Out



VP4A0095	SECTION	VP4A0155	SECTION	VP4A0235	SECTION
5	19 to 20	5	19 to 20	5	19 to 20
10	21 to 22	10	21 to 22	10	21 to 22
20	23 to 24	20	23 to 24	20	23 to 24
20	25 to 26	40	25 to 26	40	25 to 26
40	27 to 28	80	27 to 28	80	27 to 28
GND	1 to 18	GND	1 to 18	80	29 to 30
				GND	1 to 18

VP4A0315	SECTION	VP4A0335	SECTION
5	19 to 20	5	19 to 20
10	21 to 22	10	21 to 22
20	23 to 24	20	23 to 24
40	25 to 26	40	25 to 26
80	27 to 28	80	27 to 28
160	29 to 30	80	29 to 30
GND	1 to 18	160	31 to 32
		GND	1 to 18

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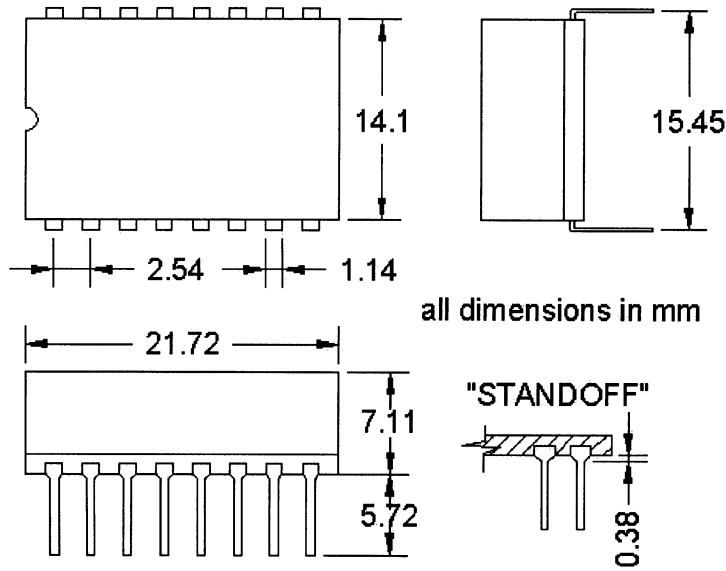
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## Dual-In-Line Delay Networks for Video Applications

### VP6 Dimensions & Pin Out



VP6A0009	SECTION	VP6A0050	SECTION	VP6A0075	SECTION
0.5	1 to 2	1	1 to 2	5	1 to 2
1	3 to 4	2	3 to 4	10	3 to 4
2	5 to 6	2	5 to 6	20	5 to 6
2	7 to 8	5	7 to 8	40	7 to 8
4	9 to 10	10	9 to 10	GND	15,16
GND	15,16	10	11 to 12		
		20	13 to 14		
		GND	15,16		

VP6A0095	SECTION	VP6A0155	SECTION
5	1 to 2	5	1 to 2
10	3 to 4	10	3 to 4
20	5 to 6	20	5 to 6
20	7 to 8	40	7 to 8
40	9 to 10	80	9 to 10
GND	15,16	GND	15,16

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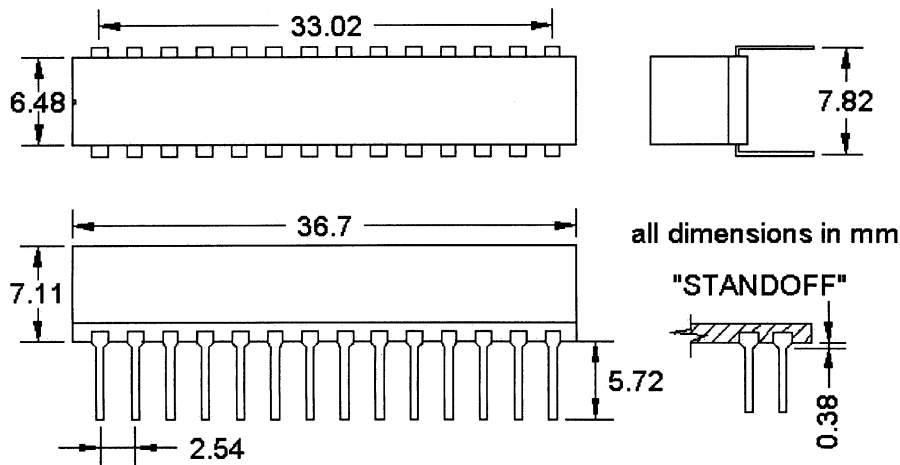
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## Dual-In-Line Delay Networks for Video Applications

### VP7 Dimensions & Pin Out



VP7A0075	SECTION	VP7A0095	SECTION
5	15 to 16	5	15 to 16
10	17 to 18	10	17 to 18
20	19 to 20	20	19 to 20
40	21 to 22	20	21 to 22
GND	1 to 14	40	23 to 24
		GND	1 to 14

VP7A0155	SECTION	VP7A0235	SECTION
5	15 to 16	5	15 to 16
10	17 to 18	10	17 to 18
20	19 to 20	20	19 to 20
40	21 to 22	40	21 to 22
80	23 to 24	80	23 to 24
GND	1 to 14	80	25 to 26
		GND	1 to 14